IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: CHRIS SPEYER

Serial No.: 10/764,777

Confirmation No.: 9740

Filed: January 26, 2004

For: MULTI-STAGE EPI PROCESS FOR FORMING SEMICONDUCTOR

DEVICES, AND RESULTING DEVICE

Patent No. 7,141,478 B2

Issue Date: November 28, 2006

Group Art Unit: 2822

Examiner: Michael Manh Trinh

Att'y Docket: 2069.012900

Customer No.: 23720

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

The patentee requests a certificate of correction for Patent and Trademark Office ("PTO") errors pursuant to 37 C.F.R. § 1.322. Because this error was the PTO's, no fee should be required. If any fee is required, please charge Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.115600.

The following error is listed on the attached form PTO-1050.

Col. 10, line 45 (claim 17, line 21), change "perfonning" to -- performing --.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON CUSTOMER NO. 23720

Date: December 4, 2006

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,141,478 B2

DATED : November 28, 2006 INVENTOR(S) : Chris Speyer

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below;

Col. 10, line 45 (claim 17, line 21), change "perfonning" to -- performing --.

MAILING ADDRESS OF SENDER: J. Mike Amerson WILLIAMS, MORGAN & AMERSON, P.C. 10333 Richmond, Suite 1100 Houston, Texas 77042 PATENT NO.7,141,478 B2 No. of add'l. copies @ 50¢ per page